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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Application / Conf. No.</b>	10/659,978 / 5060
		<b>Filing Date</b>	September 11, 2003
		<b>First Named Inventor</b>	Eric D. Groen
		<b>Art Unit</b>	2817
		<b>Examiner Name</b>	Arnold M. Kinkead
<b>Sheet</b>	2 of 2	<b>Attorney Docket Number</b>	X-1433 US

OTHER – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		John D. van der Tang et al.; "A 9.8-11.5 GHz Quadrature Ring Oscillator for Optical Receivers"; 2002 IEEE; IEEE Journal of Solid-State Circuits, Vol. 37, No. 3; March 2002; pp. 438-442.	
		Seong-Jun Song et al.; "A 4-Gb/s CMOS Clock and Data Recovery Circuit Using 1/8-Rate Clock Technique"; 2003 IEEE; IEEE Journal of Solid-State Circuits, Vol. 38, No. 7; July 2003; pp. 1213-1219.	
		Werner Baumberger; "A Single Chip Image Rejecting Downconverter for the 2.44 GHz Band"; Gallium Arsenide Integrated Circuit (GAASIC) Symposium; 1993; 15th Annual; 10-13 October 1993; pp. 37-40.	
		A Worapishet et al.; "An NMOS Inductive Loading Technique for Extended Operating Frequency CMOS Ring Oscillators"; The 2002 45th Midwest Symposium on Circuits and Systems; Conference Proceedings; August 4-7, 2002; IEEE, Vol. 1 of 3; 4 August 2002; pp. I-116 to I-119.	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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